

S/N 09/964,010

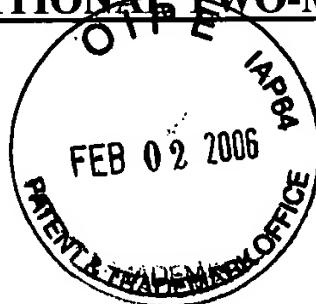
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Matthew B. Haycock et al. Examiner: Tse Chen
Serial No.: 09/964,010 Group Art Unit: 2116
Filed: September 26, 2001 Docket No: 884.455US1
Title: METHOD AND APPARATUS FOR REALIGNING BITS ON A PARALLEL BUS
Assignee: Intel Corporation Customer Number: 21186

PETITION FOR AN ADDITIONAL TWO-MONTH EXTENSION OF TIME

MS RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450



In accordance with the provision of 37 CFR § 1.136(a), it is respectfully requested that an additional two-month extension of time be granted in which to respond to the Final Rejection mailed July 29, 2005, said period of response being extended from October 29, 2005 to January 29, 2006 (Sunday). A one month extension of time, and requisite fee, were filed with the Amendment and Response on November 29, 2005.

Please charge Deposit Account No. 19-0743 in the amount of \$900.00 to cover the required additional extension fee. Please charge any additional fees or credit overpayment to deposit Account No. 19-0743.

Respectfully Submitted

MATTHEW B. HAYCOCK ET AL.

02/03/2006 CCHAU1 00000088 190743 09964010
01 FC:1253 900.00 DA

By their Representatives,

By their Representatives,
SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
Attorneys for Intel Corporation
P.O. Box 2938
Minneapolis, Minnesota 55402
(612) 373-6969

Date: 30 January 2006 By: *Viet V. Tong*

Viet V. Tong
Reg. No: 45,416

CERTIFICATE UNDER 37 CFR § 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 30th day of January 2006.

Name: Amy Moriarty Signature: *Amelia*